

Fig. 1 is a cross-sectional view of a semiconductor device. It shows a substrate with layers 1, 12, and 18. A central region contains a patterned layer 6. Above this, there are several layers: 16, 4, 8, 10, 14, and 20. A top layer 22 is on the right. The device is labeled II and II' at the bottom.

FIG.3A
RELATED ART

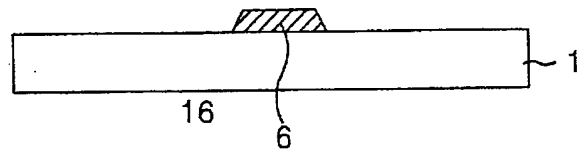


FIG.3B
RELATED ART

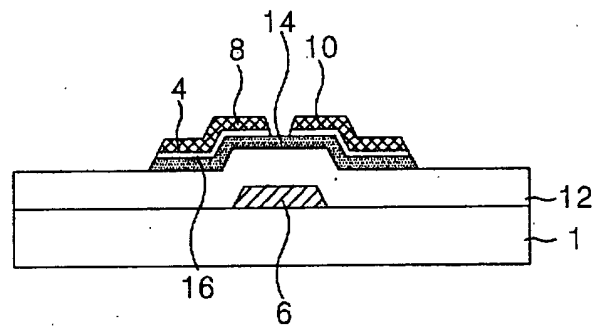


FIG.3C
RELATED ART

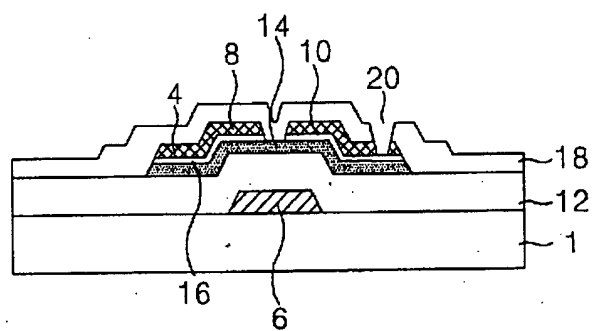


FIG.3D
RELATED ART

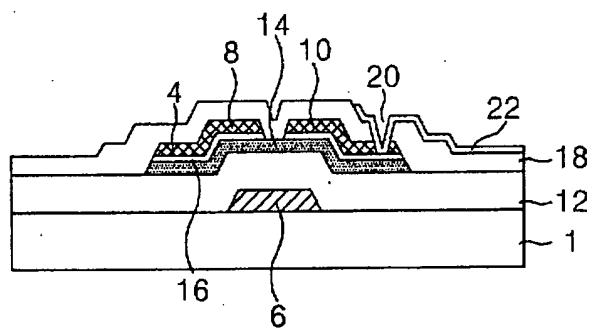


FIG.4
RELATED ART

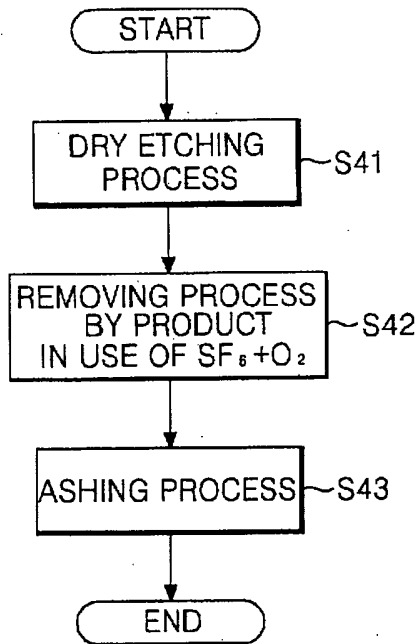


FIG.5

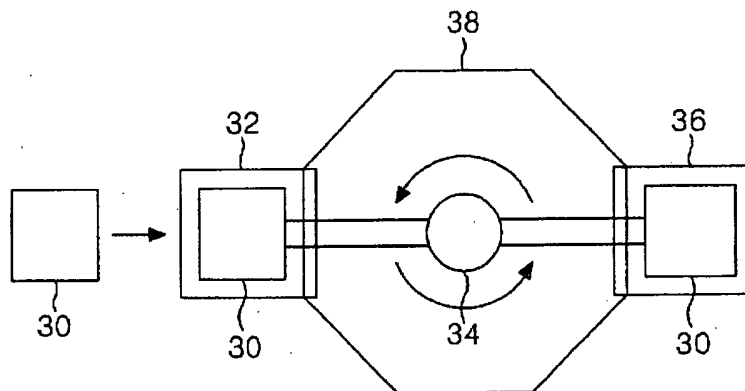


FIG. 6

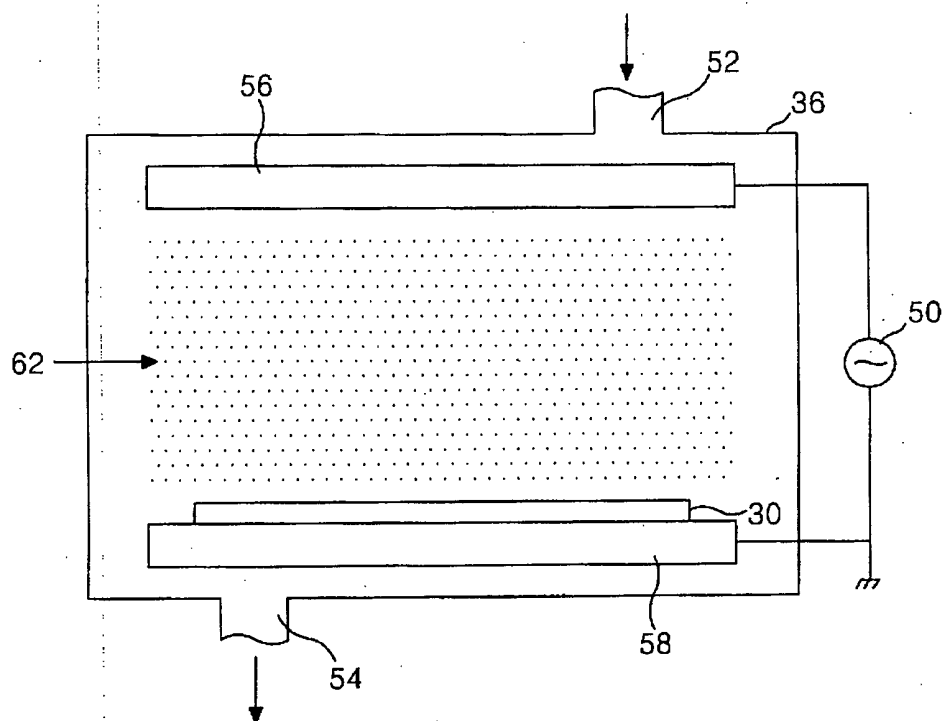


FIG.8

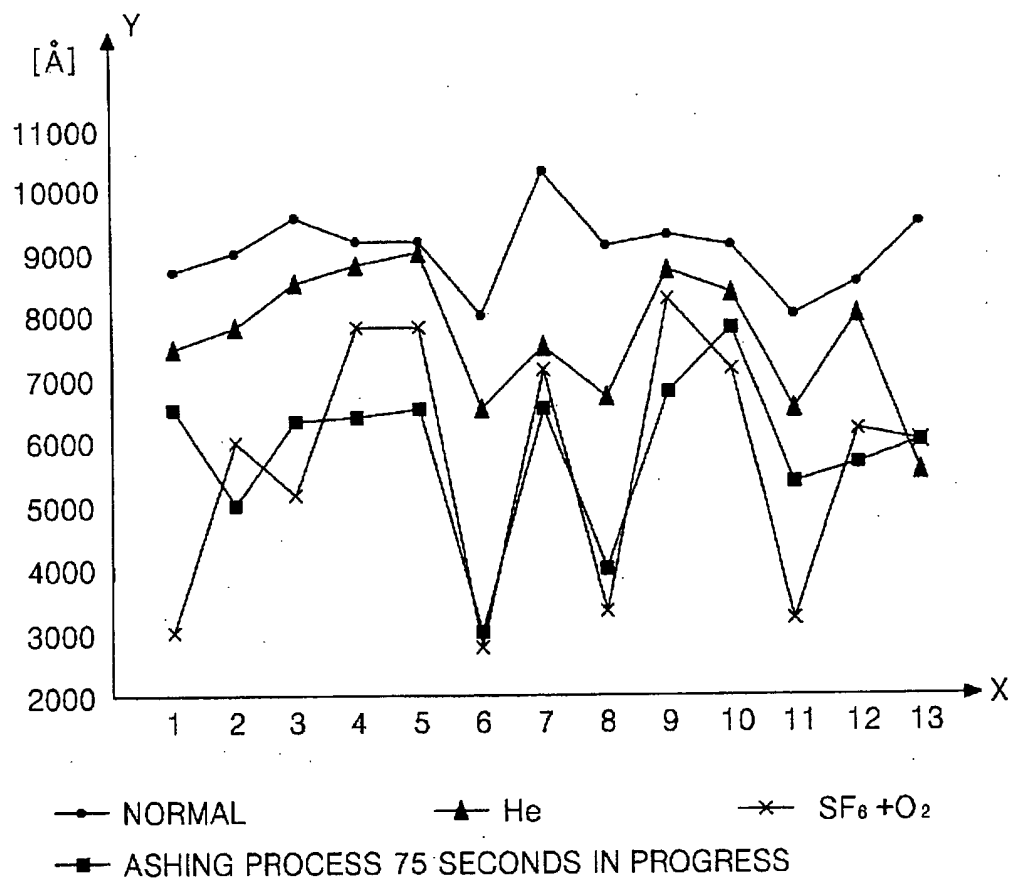


FIG.7

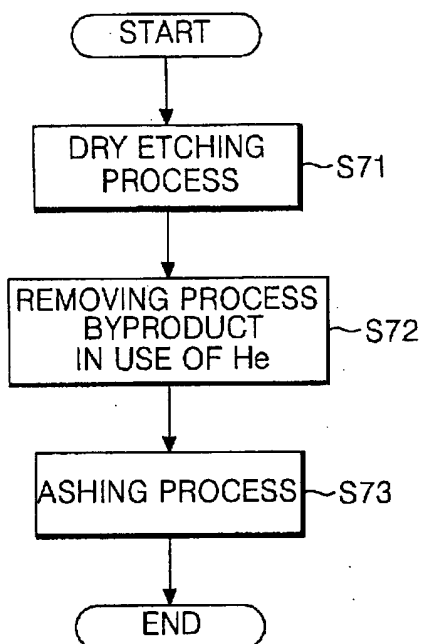


FIG.9

